

**REMARKS**

This paper is a response to the Office Action mailed on May 28, 2008. Claims 1-4, 6-9, 11-15, and 18-24 are pending in this application. Claims 1, 6, 12, and 20 are amended. No claims are cancelled or added. Accordingly, claims 1-4, 6-9, 11-15, and 18-24 remain pending in this application.

The Office Action rejected claims 1-4, 6-9, 11-15, and 18-24. More specifically, the status of the application in light of this Office Action is as follows:

(A) Claims 1-4, 6-9, 11-15, and 18-24 were rejected under 35 U.S.C. 102(b) as being anticipated by U.S. Patent 6,223,322 ("Michigami").

A. **Response to Section 102 Rejection of Claims 1-4, 6-9, 11-15, and 18-24**

Amended independent claim 1 is respectfully submitted to be allowable at least because Michigami fails to disclose or suggest "mapping the block index to a physical address of a memory based on the block index value and a number N, wherein N is a number of banks of the memory and is an integer value more than two," (emphasis added) as recited by applicant's claim 1.

As discussed on page 9 of applicants' amendment of February 21, 2008, applicant's technology functions with any number of memory banks. To do so, applicant's technology computes physical memory addresses based on a block index value and the number of banks (N). (See, e.g., applicant's fig. 4 and paras. [0030]-[0033]). In contrast, Michigami is generally directed to memory management of ECC product-coded data arrays. To accomplish this goal, Michigami employs an interleaved pair of memories/memory banks. (See, e.g., Michigami, Abstract, fig. 3, col. 5, lines 36-42, fig. 5, and col. 6, lines 42-45). Moreover, the addressing scheme of Michigami's table 2 and col. 8, line 62 – col. 9, line 43 provides only a single bank select bit. Accordingly, this addressing scheme is able to address, and thus employ, only two

banks. Applicant respectfully submits that claim 1 is allowable for at least these reasons.

Amended independent claim 6 is respectfully submitted to be allowable at least because Michigami fails to disclose or suggest "dividing the block index value by N for acquiring a quotient Q and a remainder R, wherein N is a number of banks of the memory that is more than two[.]" as recited by applicant's claim 6.

Amended independent claim 12 is respectfully submitted to be allowable at least because Michigami fails to disclose or suggest "wherein the memory includes more than two banks[.]" as recited by applicant's claim 12.

Amended independent claim 20 is respectfully submitted to be allowable at least because Michigami fails to disclose or suggest "wherein N is a number of banks of the memory that is more than two," as recited by applicant's claim 20.

Each of the remaining claims depends from one of the above discussed independent claims. They are respectfully submitted to be allowable for at least these reasons.

Conclusion

In view of the foregoing, the pending claims comply with the requirements of 35 U.S.C. § 112 and are patentable over the applied art. The applicants accordingly request reconsideration of the application and a mailing of a Notice of Allowance. If the Examiner has any questions or believes a telephone conference would expedite prosecution of this application, the Examiner is encouraged to contact Davin Chin at (206) 359-6196.

Respectfully submitted,  
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